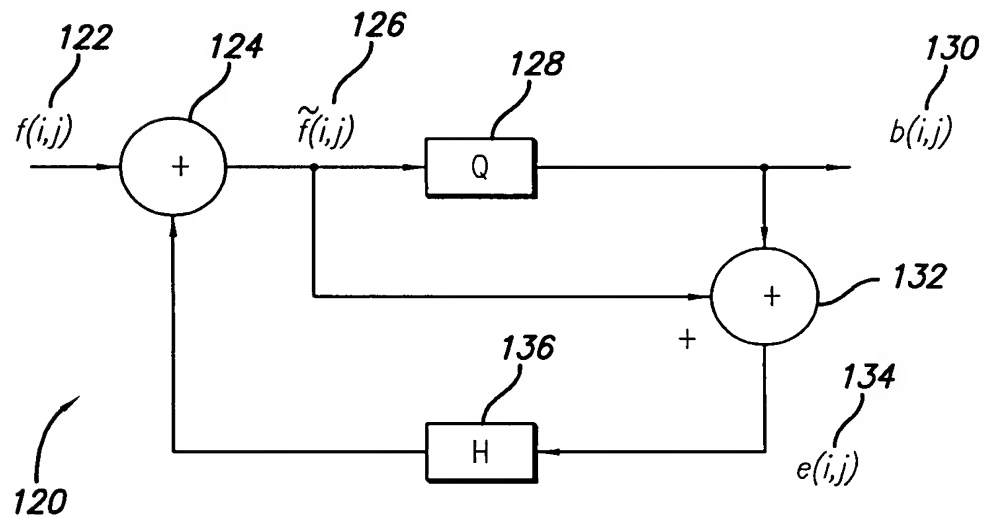


SAMPLE SCAN PROGRESSION USING ERROR DIFFUSION

FIG. 1A



GENERIC ERROR DIFFUSION LOGIC DIAGRAM

FIG. 1B

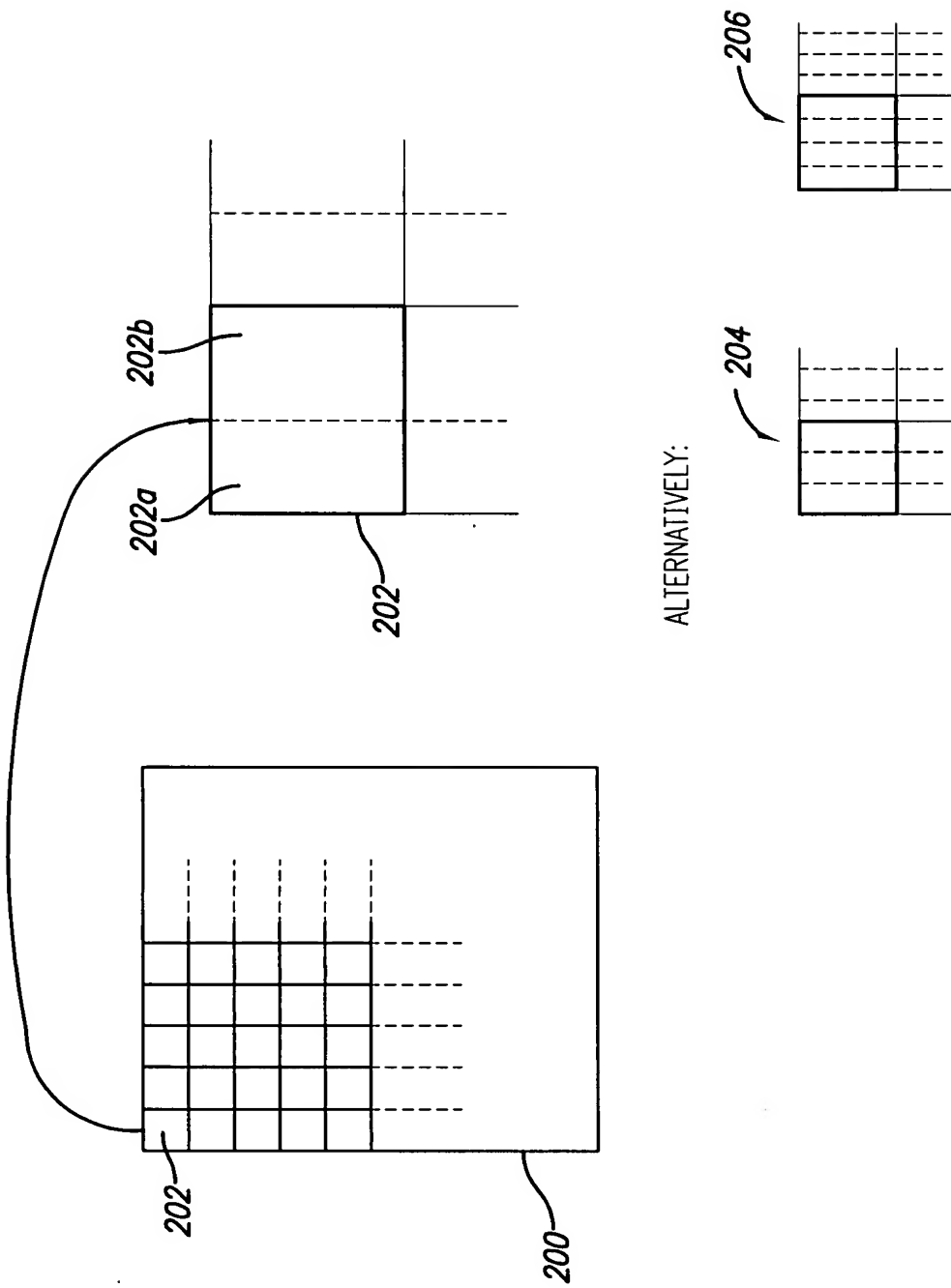


FIG. 2

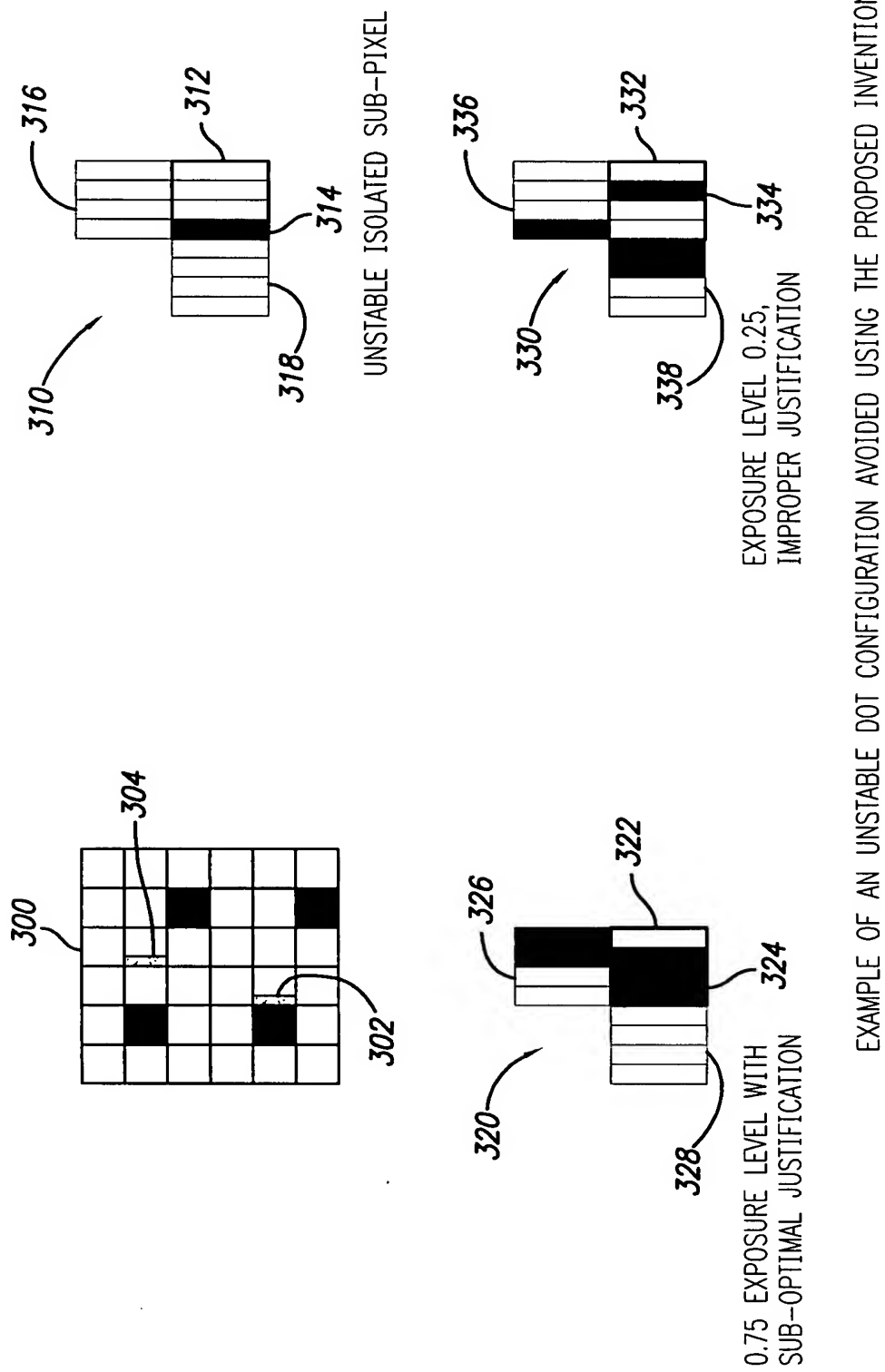


FIG. 3

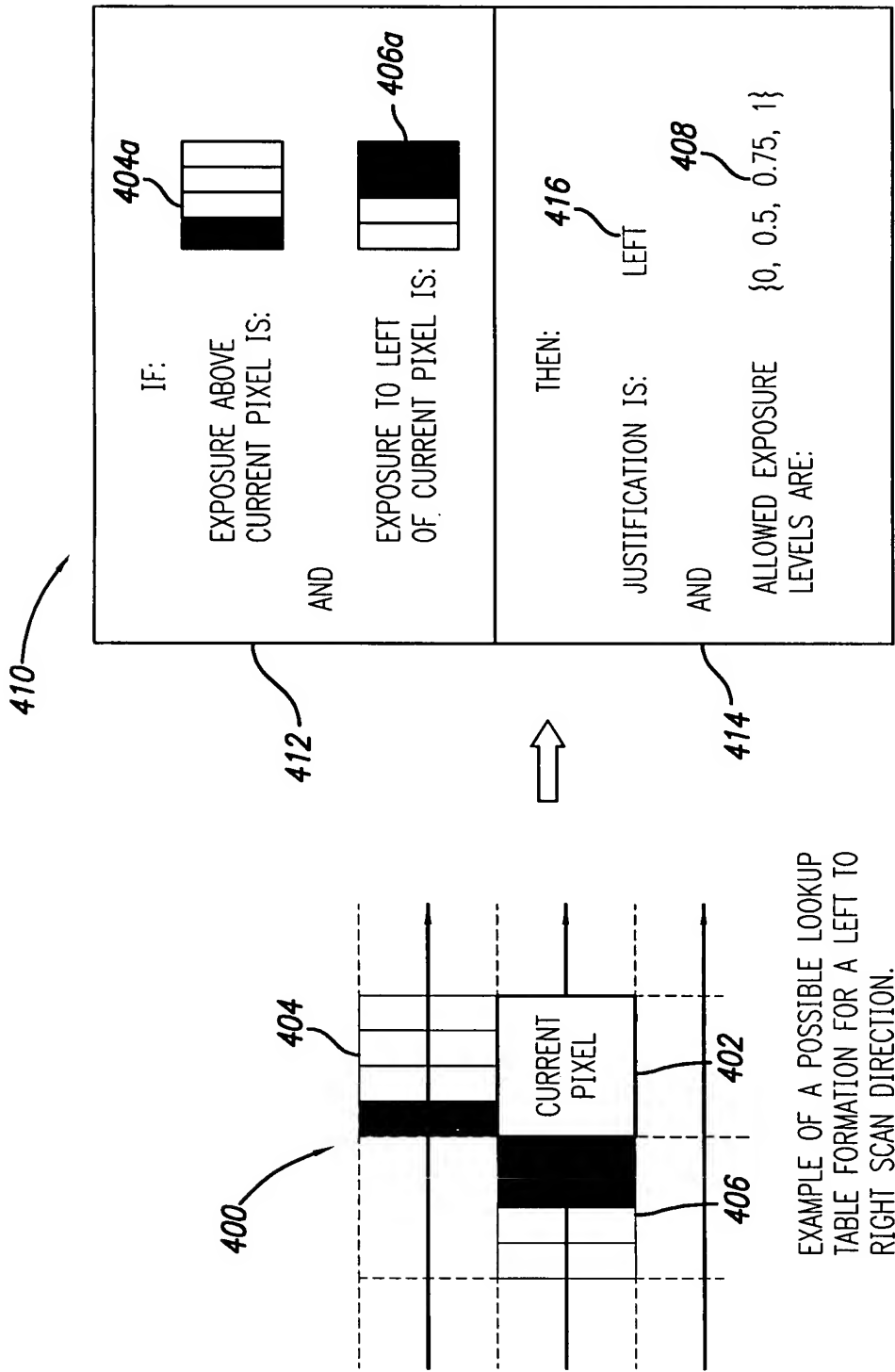
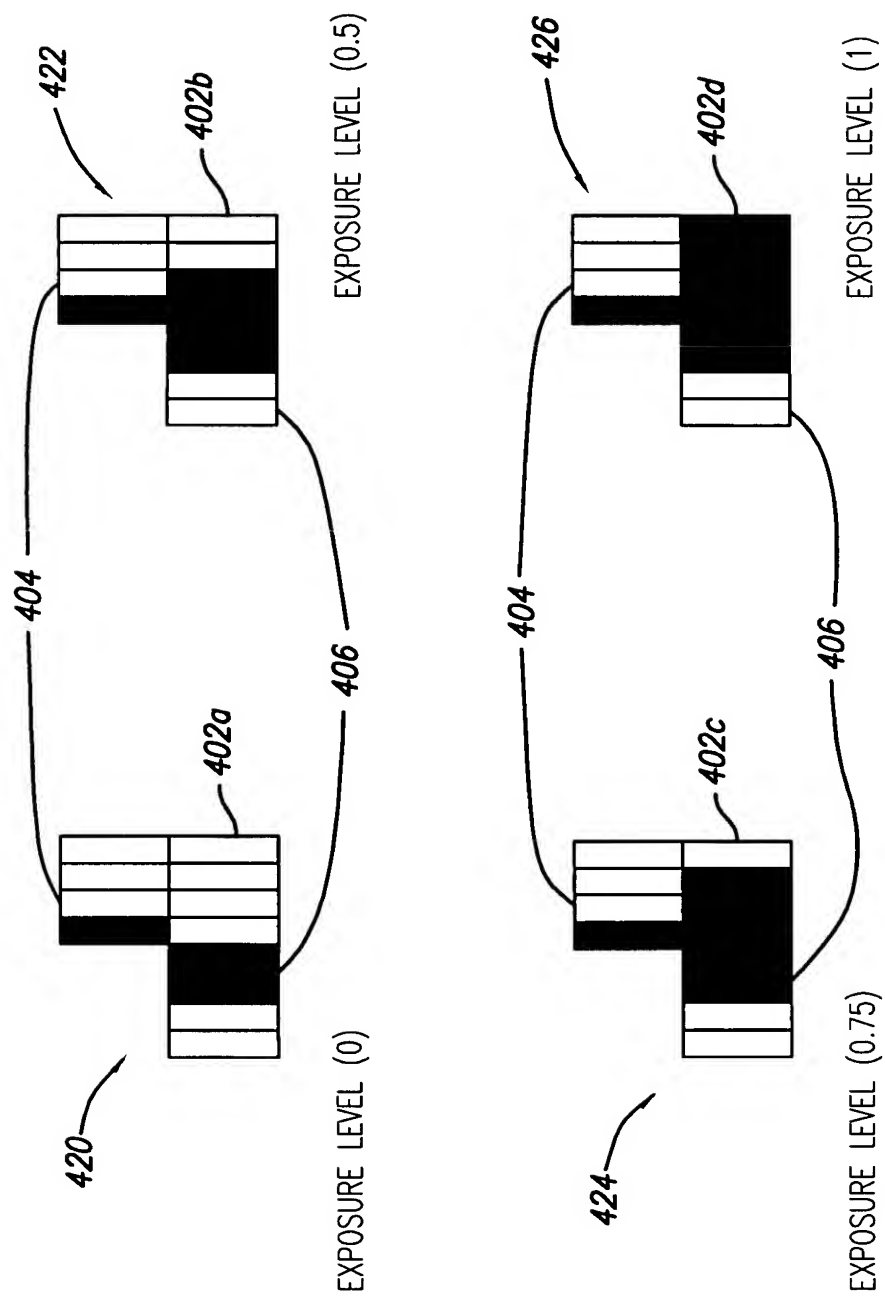


FIG. 4A



POSSIBLE DOT CONFIGURATIONS USING THE ENCODING OF FIG. 4A

FIG. 4B

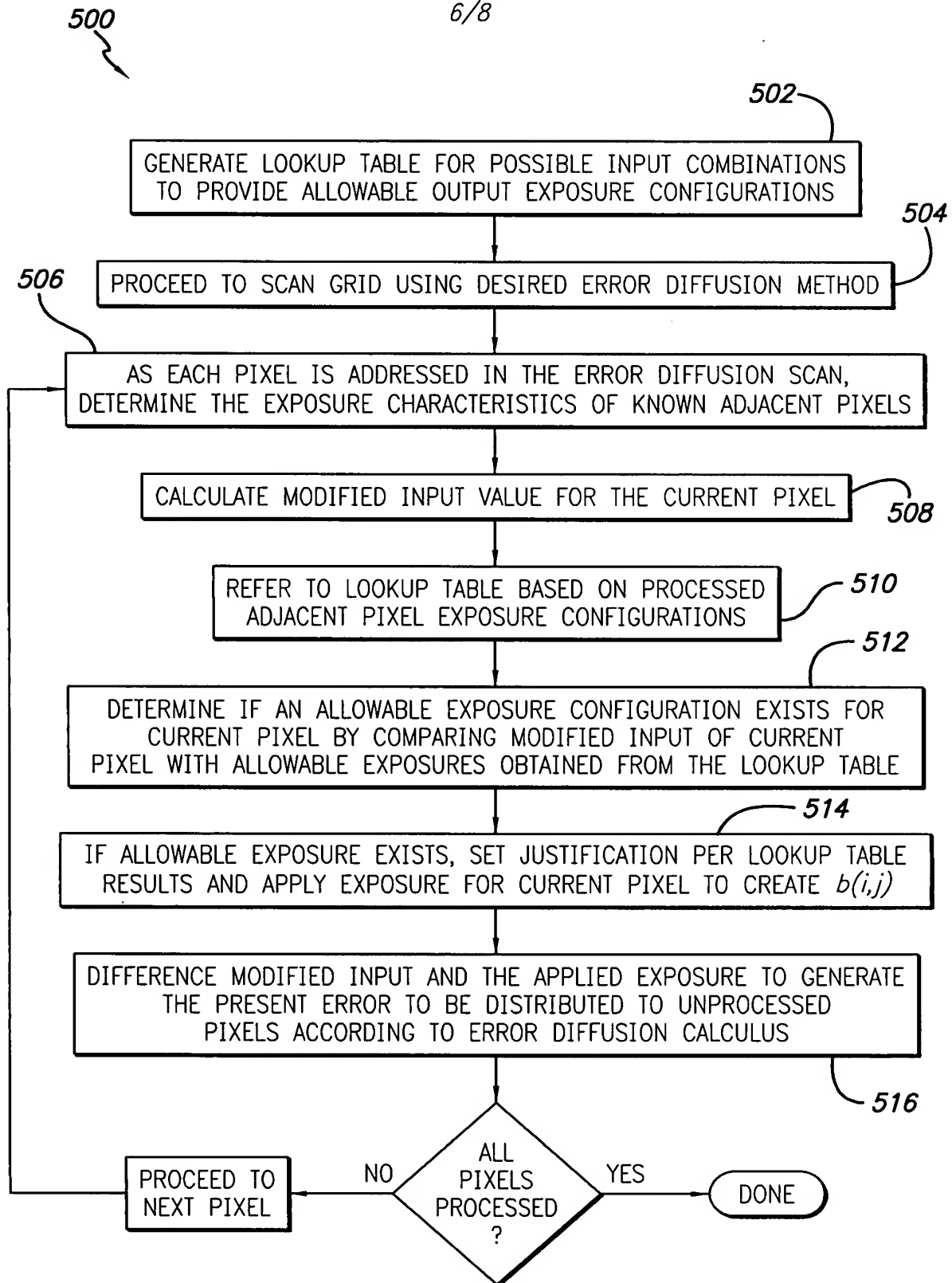


FIG. 5